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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: : Date: 10/8/2003
MOHAMED IMAM et al : Group: 2812
APPLN. NO. 09/917,731 : Examiner: Angel Roman
FILED: 07/31/2001 :

FOR: METHOD FOR MANUFACTURING A HIGH VOLTAGE MOSFET SEMICONDUCTOR DEVICE
WITH ENHANCED CHARGE CONTROLLABILITY

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE
UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE
ADDRESSED TO: COMMISSIONER OF PATENTS AND TRADEMARKS
Alexandria, V.A. 22313-1450, ON: 10/8/03

Lydia McNamara SEMICONDUCTOR COMPONENTS INDUSTRIES, L.L.C.
Name of Person Signing Certificate Name of Assignee
Lydia McNamara 10/8/03
SIGNATURE DATE

Honorable Commissioner of Patents and Trademarks,
Alexandria, V.A. 22313-1450

Attn: Official Draftsperson

SIR:

We are hereby submitting ten (10) sheets of formal drawings for the
above-identified patent application per 37 CFR 1.85 as required by the
Examiner in the Office Action dated July 14, 2003.

Respectfully submitted,

Mohamed Imam et al

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